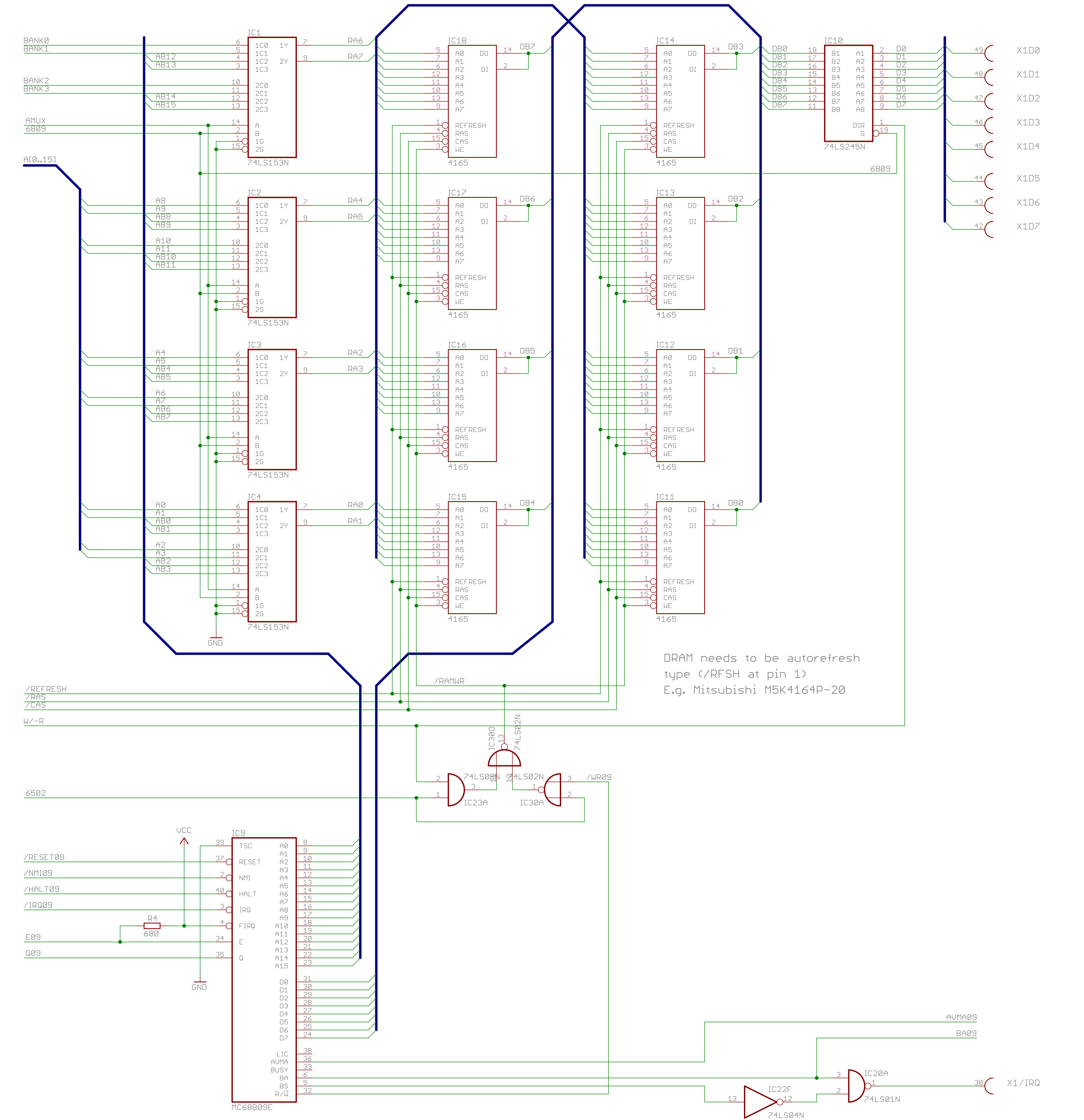
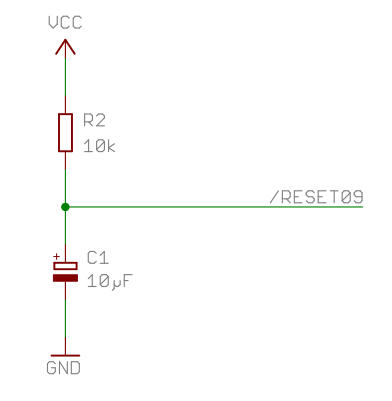


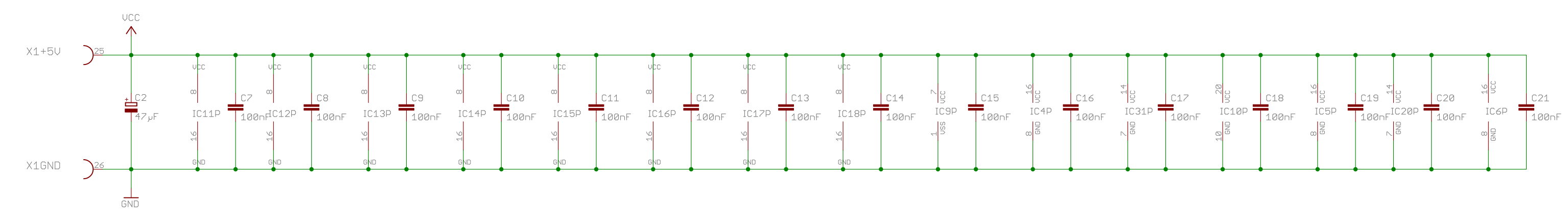
\$C0N0: generate IRQ to 6809 / do single step when halted
 \$C0N1: map ROM
 \$C0N2: map RAM \$0000 - 0FFF
 \$C0N3: map RAM \$4000 - 4FFF
 \$C0N4: map RAM \$1000 - 3FFF
 \$C0N5: map RAM \$5000 - 7FFF
 \$C0N6: map RAM \$9000 - BFFF
 \$C0N7: map RAM \$D000 - FFFF

 \$C0N8: generate NMI to 6809
 \$C0N9: map ROM
 \$C0NA: map RAM \$8000 - 8FFF
 \$C0NB: map RAM \$C000 - CFFF
 \$C0NC: map RAM \$1000 - 3FFF
 \$C0ND: map RAM \$5000 - 7FFF
 \$C0NE: map RAM \$9000 - BFFF
 \$C0NF: map RAM \$D000 - FFFF

N is slot number +8. Default for IBS software is slot 4.
 Internal RAM substitutes \$D000 - FFFF ROM area.
 Write C0Nx for R/W, read C0Nx for R only access.
 6809 generates IRQ to Apple when SYNC opcode is executed.



DRAM needs to be autorefresh type (/RFSH at pin 1)
 E.g. Mitsubishi MSK4164P-20



Adjust trim pots for best E-Q symmetry at IC9 pin 34, 35.